

Exhibitor	Booth	Description
Abaco Systems	18	Abaco Systems, formerly GE Intelligent Platforms, will demonstrate the FORCE2 COTS mission computer featuring the SBC314 OpenVPX single board computer with the NXP T1042/2081 QorIQ(tm) processor. FORCE2 runs Wind River's VxWorks 653 flight certifiable and FACE aligned Operating System, bringing high performance to low power rugged avionics applications.
AMRDEC & PEO Aviation	7	The demonstration in the Army booth includes the Data Correlation and Fusion Manager (DCFM) UoPs developed by Boeing-Sikorsky and Honeywell for the Joint Common Architecture (JCA) demo, several PSS UoPs for controlling different sensors, and Fuel Calculation and Engine Instrumentation and Crew Alerting System (EICAS) UoPs currently in-development for the Crew Mission Station (CMS) effort. This software is integrated into the Modular Integrated Survivability (MIS) system developed by AMRDEC's Aviation Systems Integration Facility (ASIF) team.
Boeing	1	Phantom Fusion (PF) - provides an open system, general purpose computing solution that can be optionally configured to support security (MLS/IA/AT) requirements and supports FACE profiles. PF is highly aligned with the commercial computing industry by leveraging COTS (hardware and software), VITA standards and Internet Protocols.
CES	17	Creative Electronics Systems will be showing its ROCK-2 (3U VPX) Safety-Certifiable and ROCK-3 (VNX) SWaP-Optimized Application-Ready Mission Computers, suitable for FACE software development and deployment. CES has a long history of providing Wind River's VxWorks 653 operating system and is currently shipping systems that have been certified to DO-178B Level A.
DDC-I / OAR	4	DDC-I and OAR are demonstrating the integration of Deos with RTEMS as a FACE candidate operating system. The integration of RTEMS in a Deos time partition delivers the best of both worlds, an OS with POSIX and ARINC-653 interfaces targeting the FACE Safety Base Profile in a DO-178C certifiable package.
DornerWorks	25	DornerWorks will be showcasing the ARINC653 Real-time Linux on Xen (ARLX) hypervisor on a multi-core embedded platform. This platform demonstrates the ability of ARLX to safely [and securely] execute multiple concurrent Linux guest operating systems and their respective avionics applications by providing ARINC653 compliant partitioning of hardware resources.
Esterel	27	Esterel provides SCADE System, based on the FACE metamodel, to describe the architecture, SCADE Suite to design controls UoPs, and SCADE Display for HMI UoPs. The SCADE code generators are certified for DO-178C. Esterel provides a complete solution for developing ARINC 661 user applications and the automatic generation of the A661 server within the graphical services.
FACE / UCS Alignment / Adjacent Standards	30	NSR/TRG will showcase how thoroughly documented context can be leveraged to substantially reduce the costs and risks associated with integration. We will demonstrate how simply and quickly this documentation can be captured and built, and subsequently utilized in the automation of FACE verifiable Model and Transport Segment content generation. Say goodbye to manual integration, Context is Everything.
Green Hills	14	GHS INTEGRITY-178 tuMP operating system supports Intel, ARM & QorIQ/PowerPC multicore architectures. As the flagship RTOS for high assurance FACE aligned requirements; tuMP's functional capabilities, service history, and certification pedigree provide a trusted and effective solution for obtaining optimal SWaP reductions when integrating 32/64bit multicore SOCs into critical airborne systems.
Harris Corporation	22	Harris FliteScene is a portable, full featured digital map product that works on a variety of systems. This demo shows FliteScene using FACE data model interfaces to communicate with a simulated legacy aircraft communicating with the FACE environment through a proxy. It demonstrates the ease of bringing the FACE standard into legacy systems.
Honeywell	24	Honeywell is showcasing two FACE Candidate Products. A Data Correlation and Fusion Manager that aligns to the Joint Common Architecture (JCA), and an Embedded GPS/INS (EGI). Both products have been successfully integrated and tested in the Army AMRDEC Lab.
Integration Workshop Standing Committee	3	The FACE Consortium's Integration Workshop Standing Committee is demonstrating the Basic ADS-B Lightweight Software Archetype (BALSA) and FACE data model, which is a simple, open source, reference example of an application aligned to the FACE Technical Standard. The data model passes verification tests suite v2.3.1.
Kaman Precision Products	28	Kaman's Model 9740 Multi-Port product is a small, lightweight, low power solid-state system designed to provide reliable data storage and high speed data transfer operations while withstanding the harsh demands of military environments. The Multi-Port design incorporates four removable SATA cards (up to 1TB capacity) and provides 256-bit AES Data Encryption."

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<b>LDRA</b>	<b>5</b>	LDRA Technology will be demonstrating solutions that facilitate FACE conformance and DO 178 B/C compliance. Topics will cover traceability, verification, static and dynamic code analysis solutions that automate and facilitate the generation of certification and conformance evidence.
<b>Lockheed Martin</b>	<b>9</b>	Lockheed Martin will be demonstrating our Open Computing Environment hosting multiple FACE Candidate UoCs. The UoCs integrate to provide a complete avionics solution, including the operating environment, CNS/ATM, communications/datalink, automated route replanning, and situational awareness, all with pedigree from previous deployments and designed for fixed/rotary wing, manned/unmanned solutions.
<b>Lynx Software Technologies</b>	<b>10</b>	Integration of off the shelf products into a full-featured, DO-178C and DO-254 certifiable, and FACE aligned cockpit display demo. Each software component is or can be certified to DO-178C DAL A and is aligned with the FACE standard as currently released. The hardware is DO-254 certifiable.
<b>NAVAIR</b>	<b>11</b>	NAVAIR will be demonstrating a command line demonstration aligned to both the FACE and OMS standards and multiple Modeling Tools for FACE including the government funded and Vanderbilt developed Software Developer's toolKit and Integrator's ToolKit. In addition, data modeling experts will be present to discuss the FACE Data Architecture and whiteboard solutions for data modeling challenges.
<b>Northrop Grumman</b>	<b>26</b>	Northrop Grumman will present our implementation of the UH-60V FACE Data Model for Units of Portability (UoPs), developed using SCADE System Configurator. We will demonstrate FACE conformance for specific UH-60V UoPs, and that our DDC middleware (IO and TSS services (core capability)) is FACE aligning.
<b>Presagis</b>	<b>29</b>	Presagis VAPS XT is a tool for development of FACE Portable Components without the need to write software, including ARINC 661 support and certifiability under DO-178. The demonstration showcases new support for multi-touch gestures; VAPS XT makes it easy to create FACE components that include the most recent post-WIMP user interfaces.
<b>Rockwell Collins</b>	<b>6</b>	Rockwell Collins is showcasing advanced software applications that bring greater capabilities to the warfighter in a more affordable manner through use of the FACE™ Technical Standard. These applications are designed to ensure full compatibility with our open architecture CAAS-based Black Hawk Flight and Mission Display System."
<b>RTI</b>	<b>15</b>	RTI will demonstrate an implementation of the FACE Transport Services Segment (TSS) using the Data Distribution Service (DDS) standard.
<b>Sikorsky Aircraft</b>	<b>31</b>	Sikorsky Aircraft will be demonstrating the results of the Army's Joint Common Architecture (JCA) demonstration program on an alternate computing platform and transport service implementation. Focus will be on use of modeling tools for development and testing.
<b>SimVentions</b>	<b>21</b>	SimVentions will demonstrate Dexter, a software integration tool that helps make the integration of FACETM components easier. Dexter graphically displays Units of Portability (UoPs), allows users to create mappings and conversions between UoPs, saves these conversions for reuse, and generates code in alignment with the FACE Technical Standard
<b>Technology Service Corporation (TSC)</b>	<b>20</b>	TSC is exhibiting a software decision aid tool for mission re-planning which has recently been successfully reconfigured as a FACE portable component.
<b>TES-SAVI</b>	<b>8</b>	Registered FACE Verification Authority since May 2014. TES-SAVi will demonstrate the TESseract processor with a FACE common operating environment for embedded development and real-time safety-critical operating systems. FACE candidate Common Control Head for Radio Control. FACE candidate Data Models and Applications prepared for FACE VA efforts. Products, Tools, Training, Capabilities, and Services.
<b>The Open Group</b>	<b>3</b>	The Open Group will provide FACE Consortium materials and membership information.
<b>TTTech North America / Wind River</b>	<b>13</b>	TTTech and Wind River will demonstrate a deterministic, FACE Aligned architecture, that guarantees mission-critical data is delivered over Time-Triggered Ethernet with microsecond network latency and jitter. TTTech's Deterministic Ethernet provides three QoS traffic classes on the same physical network. This demonstration shows redundant and fully-synchronized PFDs with seamless failover
<b>Verocel</b>	<b>16</b>	Verocel will be demonstrating the Hyperlinked, DO-178C, Level A, Certification Data Package for the RTI Data Distribution Service (DDS) implemented on VxWorks Cert. DDS may be used as the basis for the FACE Transport Services Segment (TSS). The tools used to produce and present the certification evidence will be demonstrated.
<b>Wind River / Core AVI</b>	<b>12</b>	Wind River and CoreAVI's wide range of avionics demonstration platforms aligned with the FACE Technical Standard combine Wind River's VxWorks 7 and VxWorks 653 operating systems with CoreAVI's OpenGL ES/SC and video decode driver suites based on a wide variety of AMD, ARM, Intel and NXP/Freescale multi-core processors.